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No. 4641 P. 1

SEP 26 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Sadeg M. Faris )  
SERIAL NO.: 10/719,663 ) Group Art Unit  
FILING DATE: November 20, 2003 ) 2812  
FOR: Method of Fabricating Multi Layer )  
Devices on Buried Oxide Layer Substrates ) Examiner  
TBD

Mail Stop Missing Parts  
Commissioner of Patents  
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**RESPONSE TO RESTRICTION REQUIREMENT**

Sirs:

In response to the restriction requirement mailed July 27, 2005 in the above application, Applicants respectfully submit the herein election. Applicant hereby petitions for and requests a one -month extension of time to extend the period for response up to and including September 27, 2005. The Commissioner is hereby authorized to charge the \$60.00 extension fee for a small entity to Deposit Account No. 501648. Applicant is a Small Entity.

Applicants hereby elects group Species I. Accordingly, claims 6-13 are withdrawn. A complete listing of the pending claims is presented herein.

Thereby certify that this correspondence is being facsimile  
transmitted to the United States Patent and Trademark Office,  
Facsimile number (571) 273-8300

September 26, 2005  
(Date of Transmittal)

Ralph Crispino Reg. No. 46,144  
(Name of Person Mailing Paper)

*[Signature]* 9/26/05  
Date

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